

# LIQUID CRYSTAL DISPLAY MODULE

# **Product Specification**

DENSITRON	STANDARD LCD MODULE				
PRODUCT NUMBER	LWM12864B-SERIES				
DEFINITION	Display 128*64 dots	Date 28/01/08			

INTERNAL APPROVALS						
Quality Mgr Product Mgr Project Leader Mech. Eng Electr. En						
Date:	Date:	Date:	Date:	Date:		

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## REVISION RECORD

Date	Page	Chapt.	Comment	ECR no.
28/01/2008			Initial Specification	

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### 1 PART NUMBERING SYSTEM

<u>LWM</u> <u>12864B - BW- WCY</u> \*

Densitron mono module

① ②

3

456

① Characters x Row format

② Model serials number

3 Display mode and backlight type:

A = Reflective (without backlight) =

B\* = Transflective positive LEDS array type backlight (BG, BA, BW, BR, BT ...)

B\*(E) = Transflective LEDS edge type backlight (BGE, BAE, BWE, BR...)

B = Transflective CFL

C\* = Transflective positive EL backlight (CB, CW)

D\* = Transmissive negative EL backlight (DB, DW)

E = CFL

E\* = Transmissive negative LEDS array type backlight (EG, EA, EW, ER...)

 $E^*(E)$  = Transmissive negative LEDS edge type backlight (EGE, EAE, EWE, ERE...)

(\* color LED or EL backlight = G/yellow-green, A/ amber, W/ white, R/ red, B/blue, T/ tricolour)

#### Temperature range and power supply

D = Standard temperature range; negative supply voltage required  $(0^{\circ}\text{C} + 50^{\circ}\text{C})$ 

S = Standard temperature range; on board negative voltage generator ( $0^{\circ}\text{C}\sim+50^{\circ}\text{C}$ )

H= Wide temperature range; negative supply voltage required (-20°C ~+70°C)

W= Wide temperature range; on board negative voltage generator (-20°C ~+70°C)

#### ⑤ Fluid type and compensation circuit option

NY = STN yellow-green glass, without temperature compensation circuit

CY = STN yellow green glass, with temperature compensation circuit on board

NG = STN gray glass without temperature compensation circuit

CG = STN gray glass with temperature compensation circuit

NB = STN blue glass, without temperature compensation circuit

CB = STN blue glass with temperature compensation circuit

NF = FSTN black and white glass without temperature compensation circuit

CF = FSTN black and white glass with temperature circuit on board

#### © Special code for customized features

Please refer to our commercial office



# **Remarks and definitions:**

### 1°) Display mode and backlight type:

- Reflective polarizer, no backlight, usable only in good ambient light conditions.
- Transflective polarizer uses a background backlight and a mirror reflector, usable in all lighting conditions.
- Transmissive polarizer needs the backlight switched on continuously, usable mostly in low ambient light conditions
- EL (electro-luminescent), uniform brightness, short life time (8000 hours max), needs EL inverter, low current consumption, low thickness.
- LED (light emitting diode), uniform brightness, long lifetime (100 000 hours), doesn't need inverter, reliable in vibration and shock environment, different colors available.

Array version is a direct lighting type, available in standard form, uniform and good brightness on all the active area.

Edge version is an edge lighting type providing a low consumption backlight, has a low thickness and brightness.

- Positive mode has dark pixels on a light background
- Negative mode has light pixels in a dark background and is normally used only with a transmissive polarizer as it needs a backlight on to be visible.

### 2°) Fluid type:

- STN yellow green or grey LCD has a yellow green or grey background with dark blue pixels and offers a good contrast.
- FSTN LCD is to produce very high contrast with black and white pixels using a film polarizer



# **2 MAIN FEATURES**

ITEM	CONTENTS		
Display Format	128 x 64 dots		
Overall Dimensions	75.0 x 52.7 x 8.9(MAX)		
Viewing Area	60.0 x 32.6		
LCD type	STN / FSTN		
Mode	Available in Reflective / Transflective / Transmissive		
Viewing Angle	6 o'clock		
Duty	1/64		
Driver IC	Neotec NT7107C,NT7108C		
Backlight type	None / LED		
Backlight colour	Yellow Green / White / Amber / Blue/ Green/ Red and tri- colour		
DC/DC converter	None or Included		
Operating temperature	From 0/+50°C to -20°/+70°C		
Storage temperature	From -20°/+70°C to -30°/+80°C		

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# **3 MECHANICAL SPECIFICATION**

## 3.1 MECHANICAL CHARACTERISTICS

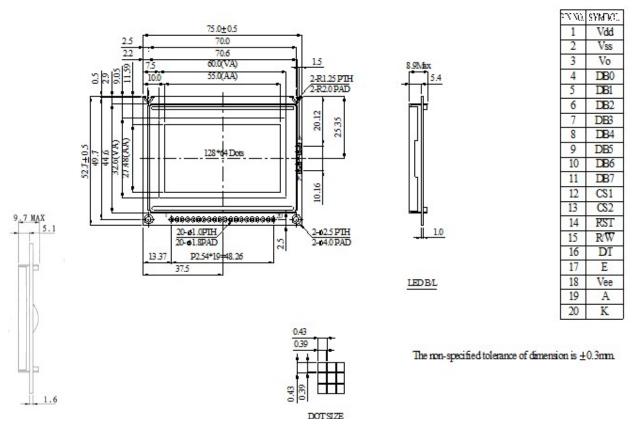
Item	Dimension	Unit
Number of Characters	128 x 64 dots	
Module dimension	75.0 x 52.7 x 8.9(MAX)	
	EL or No B/L 93 x 70.0 x 9.7(MAX)	– mm
View area	60.0 x 32.6	mm
Active area	55.0 x 27.48	mm
Dot size	0.41 x 0.41	mm
Dot pitch	0.43 x 0.43	mm

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## 3.2 MECHANICAL DRAWING

#### Version LED



DENSITRON TECHNOLOGIES plc. reserves the right to make changes to their products without notice, and advise customers to obtain the latest version of the relevant product to verify compatibility.

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# **4 ELECTRICAL SPECIFICATION**

### 4.1 ABSOLUTE MAXIMUM RATINGS

ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT
Operating Temperature	$T_{OP}$	-20	-	+70	°C
Storage Temperature	$T_{ST}$	-30	-	+80	°C
Input Voltage	V <sub>I</sub>	0		$V_{DD}$	V
Supply Voltage For Logic	$V_{DD}$	0	-	6.7	V
Supply Voltage For LCD	V <sub>DD</sub> -Vss	0	-	16.7	V
Supply Voltage For LCD	V <sub>DD</sub> -V <sub>OUT</sub>	-	-	-10	V

Note 1: Background colour changes slightly depending on ambient temperature. This phenomenon is

reversible. Ta≤70 °C: 75% RH max

Note 2: Ta≤80 °C: 75% RH max

## 4.2 ELECTRICAL CHARACTERISTICS

VSS = 0 V, Ta = 25 °C

Item	Symbol	Condition	Min	Тур	Max	Unit
Supply Voltage For Logic	$V_{DD}$ - $V_{SS}$	_	4.5	5.0	5.5	V
Supply Voltage For LCD	$ m V_{DD} ext{-}V_0$	Ta=-20°C Ta=25°C Ta=+70°C	_ _ 7.5	- 8.5 -	9.5 _ _	V V V
Input High Volt.	$V_{ m IH}$	_	2.0	_	$V_{DD}$	V
Input Low Volt.	V <sub>IL</sub>	_	0	_	0.8	V
Output High Volt.	$ m V_{OH}$	_	2.4	_	$V_{DD}$	V
Output Low Volt.	$V_{\mathrm{OL}}$	_	0	_	0.4	V
Supply Current	$I_{DD}$			2.5		mA

<sup>\*</sup> I<sub>DD</sub> measurement condition is for all pattern ON



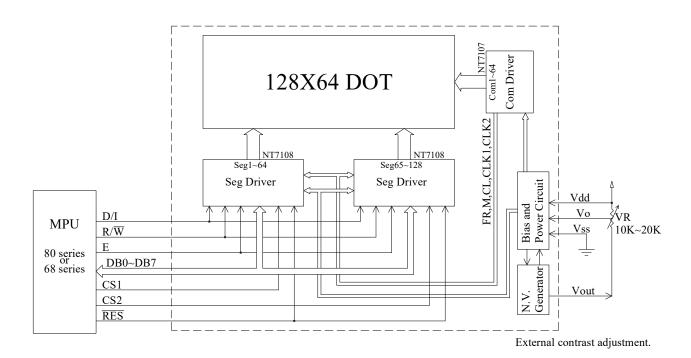
# **4.3 INTERFACE PIN ASSIGNMENT**

Pin No.	Symbol	Level	Description
1	Vdd	5.0V	Supply voltage for logic
2	$V_{SS}$	0V	Ground
3	Vo	(Variable)	Operating voltage for LCD
4	DB0	H/L	Data bit 0
5	DB1	H/L	Data bit 1
6	DB2	H/L	Data bit 2
7	DB3	H/L	Data bit 3
8	DB4	H/L	Data bit 4
9	DB5	H/L	Data bit 5
10	DB6	H/L	Data bit 6
11	DB7	H/L	Data bit 7
12	CS1	Н	Select Column 1~ Column 64
13	CS2	Н	Select Column 65~ Column 128
14	RST	L	Reset signal
15	R/W	H/L	H: Read (MPU←Module), L: Write (MPU→Module)
16	D/I	H/L	H: Data, L: Instruction
17	Е	Н	Enable signal
18	Vee	_	Negative Voltage output
19	A	_	Power Supply for LED backlight (+)
20	K	_	Power Supply for LED backlight (-)

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# 4.4 Interface Block Diagram



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### 4.5 DISPLAY CONTROL INSTRUCTION

The display control instructions control the internal state of the NT7108. Instructions are received from the MPU to the NT7108 for the display control.

The following table shows various instructions.

Instruction	RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	Function
Display on/off	L	L	L	L	Н	Н	Н	Н	Н	L/H	Controls the display on or off. Internal status and display RAM data is not affected. L:OFF, H:ON
Set address (Y address)	L	L	L	Н		Υ	addre	ss (0-6	3)		Sets the Y address in the Y address counter.
Set page (X address)	L	L	Н	L	Н	Н	Н	Pa	age (0	-7)	Sets the X address at the X address register.
Display Start line (Z address)	L	L	Н	Н		Display start line (0-63)					Indicates the display data RAM displayed at the top of the screen.
Status read	L	Н	Busy	L	On/ Off	Reset	L	L	L	L	Read status. BUSY L: Ready H: In operation ON/OFF L: Display ON H: Display OFF RESET L: Normal H: Reset
Write display data	Н	L				Write data					Writes data (DB0: 7) into display data RAM. After writing instruction, Y address is increased by 1 automatically.
Read display data	Н	Н				Read data				Reads data (DB0: 7) from display data RAM to the data bus.	

RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
0	0	0	0	1	1	1	1	1	D

The display data appears when D is 1 and disappears when D is 0. Though the data is not on the screen with D=0, it remains in the display data RAM. Therefore, you can make it appear by changing D=0 into D=1.

### SET ADDRESS (Y ADDRESS)

RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
0	0	0	1	AC5	AC4	AC3	AC2	AC1	AC0

Y address (AC0-AC5) of the display data RAM is set in the Y address counter. An address is set by instruction and increased by 1 automatically by read or write operations of display data.

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### **SET PAGE (X ADDRESS)**

RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
0	0	1	0	1	1	1	AC2	AC1	AC0

X address (AC0-AC2) of the display data RAM is set in the X address register. Writing or reading to or from MPU is executed in this specified page until the next page is set.

### **DISPLAY START LINE (Z ADDRESS)**

RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
0	0	1	1	AC5	AC4	AC3	AC2	AC1	AC0

Z address (AC0-AC5) of the display data RAM is set in the display start line register and displayed at the top of the screen. When the display duty cycle is 1/64 or others (1/32-1/64), the data of total line number of LCD screen, from the line specified by display start line instruction, is displayed.

#### STATUS READ

RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
0	1	BUSY	0	ON/OFF	RESET	0	0	0	0

#### **BUSY**

When BUSY is 1, the Chip is executing internal operation and no instructions are accepted. When BUSY is 0, the Chip is ready to accept any instructions.

#### ON/OFF

When ON/OFF is 1, the display is OFF. When ON/OFF is 0, the display is ON.

### RESET

When RESET is 1, the system is being initialized.

In this condition, no instructions except status read can be accepted.

When RESET is 0, initializing has finished and the system is in usual operation condition.

#### WRITE DISPLAY DATA

RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
1	0	D7	D6	D5	D4	D3	D2	D1	D0

Writes data (D0-D7) into the display data RAM. After writing instruction, Y address is increased by one automatically.

#### **READ DISPLAY DATA**

RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
1	1	D7	D6	D5	D4	D3	D2	D1	D0

Reads data (D0-D7) from the display data RAM. After reading instruction, Y address is increased by one automatically.

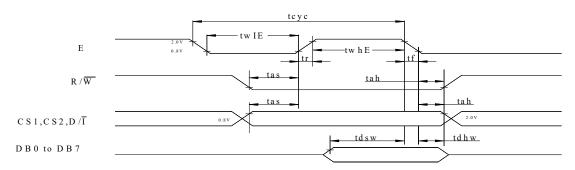
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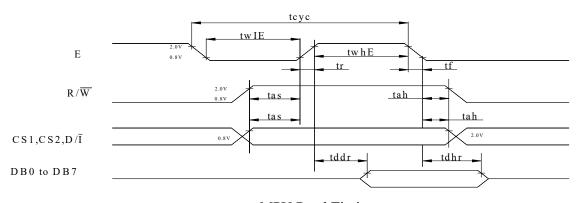
## **Timing Characteristics**

MPU Interface	$(T=25^{\circ}C, VDD=+5.0V\pm0.5)$
WII O IIIICITACC	(1 23 C, VDB \ 3.0 V \ \ 0.3)

			(		
Characteristic	Symbol	Min	Тур	Max	Unit
E cycle	tcyc	1000	_	_	ns
E high level width	twhE	450	_	_	ns
E low level width	twlE	450	_	_	ns
E rise time	tr	_	_	25	ns
E tall time	tf	_	_	25	ns
Address set-up time	tas	140	_	_	ns
Address hold time	tah	10	_	_	ns
Data set-up time	tdsw	200	_	_	ns
Data delay time	tddr	_	_	320	ns
Data hold time (write)	tdhw	10	_	_	ns
Data hold time (read)	tdhr	20	_		ns



MPU Write Timing



MPU Read Timing

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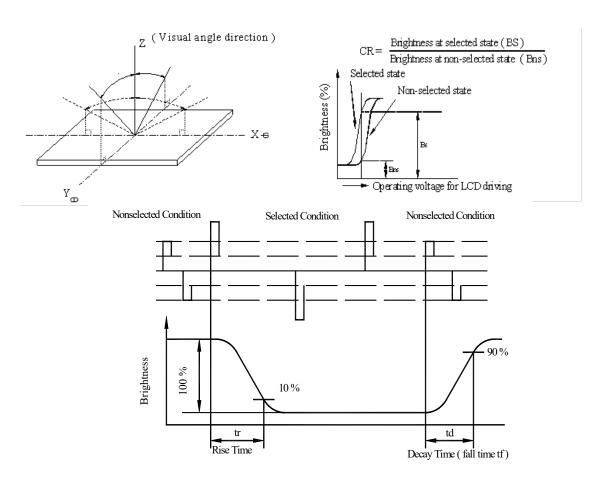


# **5 OPTICAL SPECIFICATION**

Item	Symbol	Condition	Min	Тур	Max	Unit
View Angle	(V) θ	CR≧2	10	_	105	deg
View Aligie	(H) φ	CR≧2	-30	_	30	deg
Contrast Ratio	CR	_	_	3	_	_
Response Time	T rise	_	_	200	300	ms
Response Time	T fall	_	_	200	300	ms

Definitions ■View Angles

### **■**Contrast Ratio



Definition of Operation Voltage (Vop)

Definition of Response Time (Tr, Tf).



# **6 TOUCH SCREEN SPECIFICATION**

No touch option for this module

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# **7 BACKLIGHT SPECIFICATION**

## 7.1 LED BACKLIGHT CHARACTERISTICS

### 7.1.1 WHITE STANDARD LED BACKLIGHT CHARACTERISTICS

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT	TEST CONDITION
Supply Current	ILED	_	60	90	mA	V=3.5V
Supply Voltage	V	_	3.5	3.7	V	_
Reverse Voltage	VR	_	5	_	V	_
Luminous Intensity	IV	60	_	_	cd/m <sup>2</sup>	ILED=60mA
Life Time	_	_	50K	_	Hr.	ILED=60mA

### 7.1.2 YELLOW GREEN STANDARD LED BACKLIGHT CHARACTERISTICS

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT	TEST CONDITION
Supply Current	ILED	_	100	200	mA	V=4.2V
Supply Voltage	V	_	4	4.6	V	_
Reverse Voltage	VR	_	8	_	V	_
Luminous Intensity	IV	60	_	_	CD/M <sup>2</sup>	ILED=100mA
Wave Length	λр	_	570	_	nm	ILED=100mA
Life Time	_	_	100K	_	Hr.	V≦4.6V

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# 7.1.3 AMBER STANDARD LED BACKLIGHT CHARACTERISTICS

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT	TEST CONDITION
Supply Current	ILED		100	150	mA	V=3.9V
Supply Voltage	V	_	3.9		V	_
Reverse Voltage	VR	_	8	_	V	_
Luminous Intensity	IV	16		_	cd/m <sup>2</sup>	ILED=100mA
Wave Length	λр	_	595	_	nm	ILED=100mA
Life Time	_	_	100K	_	Hr.	V≤3.9V



# **8 QUALITY ASSURANCE SPECIFICATION**

### **8.1CONFORMITY**

The performance, function and reliability of the shipped products conform to the Product Specification.

### **8.2 DELIVERY ASSURANCE**

## 8.2.1 Delivery inspection standards.

- MIL-STD-105E, general inspection level II, single sampling level;
- IPC-AA610 rev. C, class 2 electronic assemblies standard

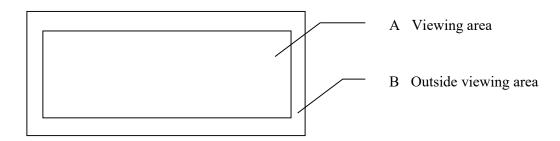
The quality assurance levels are shown below:

Rank	Item Inspected	Defect type	AQL	Remark
		Non display		
		Over current		D: 1
	Display	Missing segment	0.65%	Display malfunction
Critical defect		Wrong viewing direction		Illanunction
		Backlight OFF		
	Dimension	PCB and bezel out of	0.65%	Assembly
	Difficusion	specification	0.0376	failure
	Display	Incorrect operating		
Major defect	Do aleli alet	Flashing, dust	1.0%	
	Backlight	Wrong colour		
	LCD	Black and white spot		
		Black and white lines		
		Polariser scratch		
		Bubbles in polariser		
		Segment deformation, pin hole		
		Colour uniformity		Ammaamamaa
Minor defect		Glass chip	2.5%	Appearance defect
		Wire bond pad exposed		defect
	COB Insufficient covering with resin (wire bond line exposed)			
		Bubble, dust on COB		
	PCB Dust, solder ball on PCB			
	ICD	Pad scratch		
		Total	2.5%	

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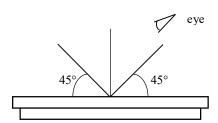


### 8.2.2 Zone definition



# 8.2.3 Visual inspection

- Inspect under 2x20W or 40W fluorescent lamp (approximately 3000 lux) leaving 25 to 30 cm between the module and the lamp and 30 cm between the module and the eye (measuring position).
- Appearance is inspected at the best contrast voltage (best contrast is adjusted considering clearness and crosstalk on screen).
- Inspect the module at 45° right and left, top and bottom.
- Use the optimum viewing angle during the contrast inspection.





# 8.2.4 Standard of appearance inspection

Units: mm

	its: mm								
No	Item	Criteria							
1	Black spot,	Round type: as per follow	ving drawing						
	white spot, dust	$\varnothing = (X+Y)/2$							
			A	cceptable quantity	<i>I</i>				
			Size	Zone A	Zone B				
		•	Ø<0.1	Any number					
		Y	0.1<Ø<0.2	6	Any number				
		→ → ↑	0.2<Ø<0.3	2	Any number				
		X	0.3<Ø	0					
		T	1 .						
		Line type: as per following		ole quantity					
		W Length	Width	Zone A	Zone B				
		Length	W≤0.02	Any number	Zone D				
		L<3.0	0.02 <w≤0.03< td=""><td></td><td>i</td></w≤0.03<>		i				
		L≤2.5	0.03 <w≤0.05< td=""><td>2</td><td>Any number</td></w≤0.05<>	2	Any number				
		L	0.05 <w< td=""><td>As round type</td><td></td></w<>	As round type					
		Total accep	table quantity: 3						
	Polariser scratch	Scratch on protective film	n is permitted						
2	1 Olarisci scratcii	Scratch on polariser: sam							
3	Polariser bubble	$\varnothing = (X+Y)/2$	<u>e us 170. 1</u>						
	1 0141110 01 0 400 010	(11.1)/2	A	cceptable quantity	7				
			Size	Zone A	Zone B				
		<b>+</b>	Ø<0.3	Any number					
		y	0.3<Ø<1.0	3	1				
		, ·	1.0<Ø<1.5	1	Any number				
		X	1.5<Ø	0					
			Total acceptable	quantity: 4					
4	Segment	1.a. Pin hole on segmente	ed display						
	deformation	XX7 1,1							
		W: segment width		. 11					
		$\emptyset = (A+B)/2$ Acceptable quantity							
		Width Ø							
			W>0.4	$\varnothing \leq 0.25$ and					
			_	quantity: 1 defect					
		Pin holes with Ø under 0.10 mm are acceptable							
		- Like							

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No	Item	Criteria		
4	Segment	1b. Pin hole on dot matrix display		
	deformation	₩ <0.05	Acceptable	quantity
			Size	
			a,b<0.1	Any number
			$(a+b)/2 \le 0.1$	Any number
			0.5<Ø<1.0	3
	Total accep			quantity: 7
		2. Segments / dots with different width		
			Accep	
			a≥b	a/b≤4/3
			a <b< td=""><td>a/b&gt;4/3</td></b<>	a/b>4/3
5	Colour	$\emptyset = (a+b)/2$ Level of sample for approval set as limit sa	Acceptable  Size $\emptyset \le 0.4$ $0.4 < \emptyset \le 1.0$ $1.0 < \emptyset \le 1.5$ $1.5 < \emptyset \le 2.0$ Total acceptable	Any number 5 3 2
3	uniformity	Level of sample for approval set as limit sample		
6	Backlight	The backlight colour should correspond to the product specification Flashing and or unlit backlight is not allowed Dust larger than 0.25 mm is not allowed		
7	СОВ	Exposed wire bond pad is not allowed Insufficient covering with resin is not allowed (wire bond line exposed) Dust or bubble on the resin are not allowed		
8	PCB	No unmelted solder paste should be present on PCB Cold solder joints, missing solder connections, or oxidation are not allowed No residue or solder balls on PCB are allowed Short circuits on components are not allowed		

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# **9 RELIABILITY SPECIFICATION**

Test Item	Test Condition	Description
High Temperature Operation	50°C or 70°C 200hrs	Endurance test applying the electric stress (Voltage & Current) and the thermal stress to the element for a long time.
Low Temperature Operation	0°C or -20°C 200hrs	Endurance test applying the electric stress under low temperature for a long time.
High Temperature Storage	70°C or 80°C 200hrs	Endurance test applying the high storage temperature for a long time.
Low Temperature Storage	-20°C or -30°C 200hrs	Endurance test applying the high storage temperature for a long time.
High Temperature & High Humidity Storage	80°C,90%RH 96hrs	Endurance test applying the high temperature and high humidity storage for a long time.
Thermal Shock Test	30°C 25°C 80°C → 30min 5min 30min For 10 cycles	Endurance test applying the low and high temperature cycle. Burn In Test.
Vibration	10~22Hz→1.5mmp-p 22~500Hz→1.5G Total 0.5hrs	Endurance test applying the vibration during transportation and using.
ESD	VS=800V,RS=1.5kΩ CS=100pF	Endurance test applying the electric stress to the terminal.
Shock Test	50G Half sign wave 11 msedc 3 times of each direction	Constructional and mechanical endurance test applying the shock during transportation.

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### 10 HANDLING PRECAUTIONS

#### Safety

If the LCD panel breaks, be careful not to get the liquid crystal fluid in your mouth or in your eyes. If the liquid crystal touches your skin or clothes, wash it off immediately using soap and plenty of water.

#### Mounting and Design

Place a transparent plate (e.g. acrylic, polycarbonate or glass) on the display surface to protect the display from external pressure. Leave a small gap between the transparent plate and the display surface.

When assembling with a zebra connector, clean the surface of the pads with alcohol and keep the surrounding air very clean.

Design the system so that no input signal is given unless the power supply voltage is applied.

#### Caution during LCD cleaning

Lightly wipe the display surface with a soft cloth soaked with Isopropyl alcohol, Ethyl alcohol or Trichlorotriflorothane.

Do not wipe the display surface with dry or hard materials that will damage the polariser surface. Do not use aromatic solvents (toluene and xylene), or ketonic solvents (ketone and acetone).

#### Caution against static charge

As the display uses C-MOS LSI drivers, connect any unused input terminal to VDD or VSS. Do not input any signals before power is turned on.

Also, ground your body, work/assembly table and assembly equipment to protect against static electricity.

#### Packaging

Displays use LCD elements, and must be treated as such. Avoid strong shock and drop from a height. To prevent displays from degradation, do not operate or store them exposed directly to sunshine or high temperature/humidity.

#### Caution during operation

It is indispensable to drive the display within the specified voltage limit since excessive voltage shortens its life. Direct current causes an electrochemical reaction with remarkable deterioration of the display quality. Give careful consideration to prevent direct current during ON/OFF timing and during operation.

Response time is extremely delayed at temperatures lower than the operating temperature range while, at high temperatures, displays become dark. However, this phenomenon is reversible and does not mean a malfunction or a display that has been permanently damaged.

If the display area is pushed on hard during operation, some graphics will be abnormally displayed but returns to a normal condition after turning off the display once.

Even a small amount of condensation on the contact pads (terminals) can cause an electro-chemical reaction which causes missing rows and columns. Give careful attention to avoid condensation.

#### Storage

Store the display in a dark place where the temperature is  $25^{\circ}\text{C} \pm 10^{\circ}\text{C}$  and the humidity below 50%RH. Store the display in a clean environment, free from dust, organic solvents and corrosive gases. Do not crash, shake or jolt the display (including accessories).